

Docket No.: 60188-116

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

H. NAKAOKA, et al.

Serial No.: Divisional of  
Appln. Serial No. 09/099,195

Filed: December 28, 2001

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Group Art Unit: To be assigned

Examiner: To be assigned

For: SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, DC 20231

Sir:

Prior to examination of the above-referenced application, please amend the application as follows:

IN THE SPECIFICATION:

On page 1, after the title, please insert the following:

--This application is a divisional of U.S. Patent application Serial No. 09/099,195, filed June 18, 1998.--.

On page 4, line 15, change "battier" to --barrier--.

**REMARKS**

The above amendment to the specification has been made to incorporate the prior

application information and the amendment made during prosecution of the parent application  
Serial No. 09/099,195. Entry of this Preliminary Amendment is respectfully requested.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



Michael E. Fogarty  
Registration No. 36,139

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**Date: December 28, 2001**  
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TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents  
Washington, DC 20231

Sir:

Twelve (12) sheets of formal drawings are submitted herewith as filed in parent application Serial No. 09/099,195. These formal drawings reflect the changes made to figures 11 - 14 in the drawing amendment being filed concurrently herewith.

It is believed that the requirements under 37 CFR 1.84 have now been satisfied for this application.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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Fig. 1

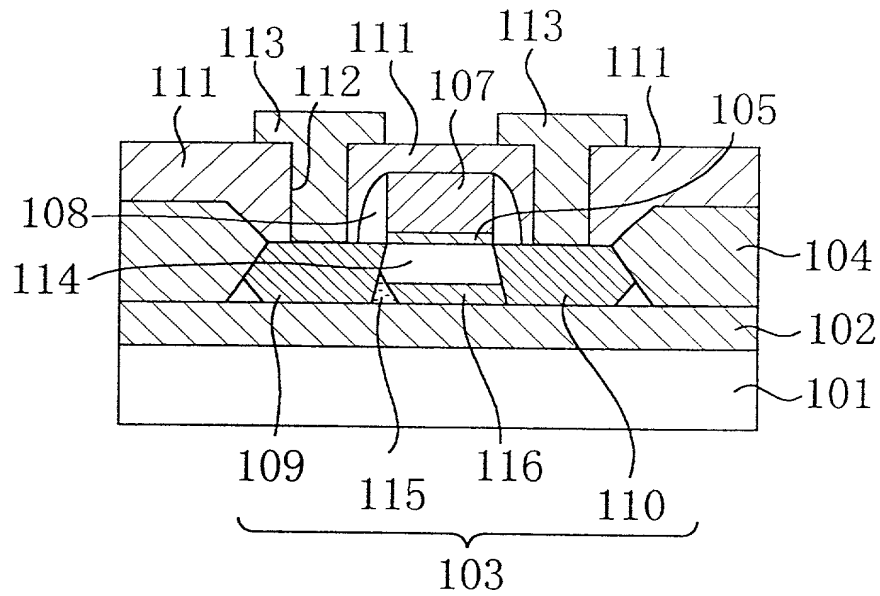


Fig. 2

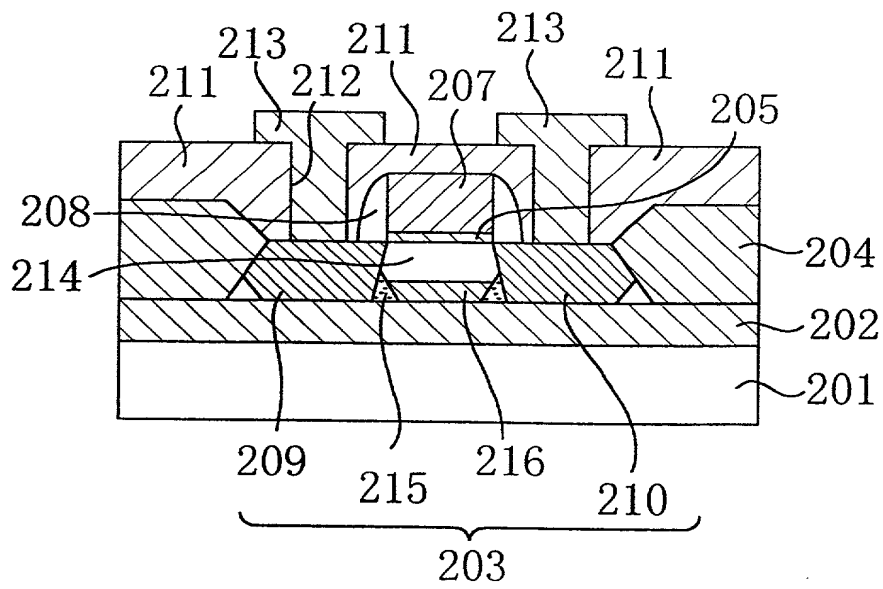
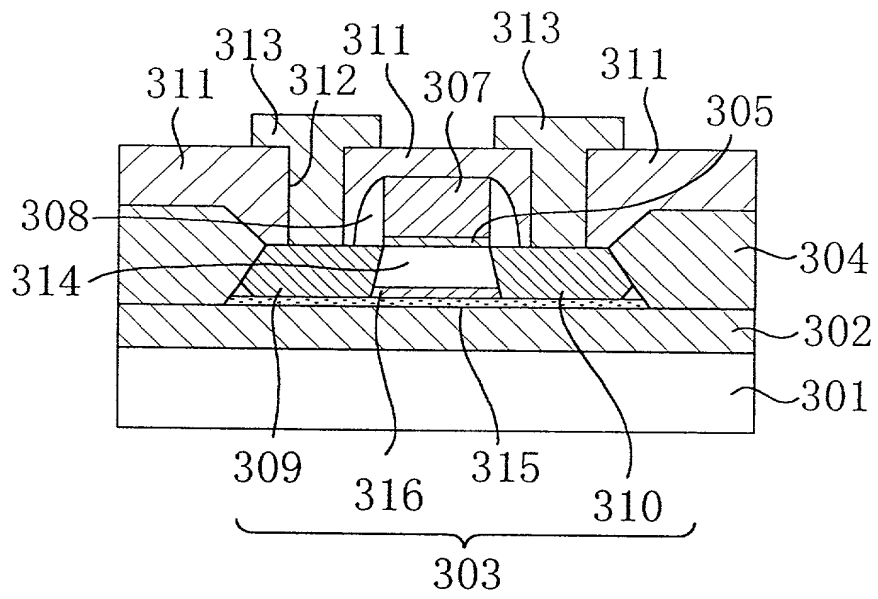


Fig. 3



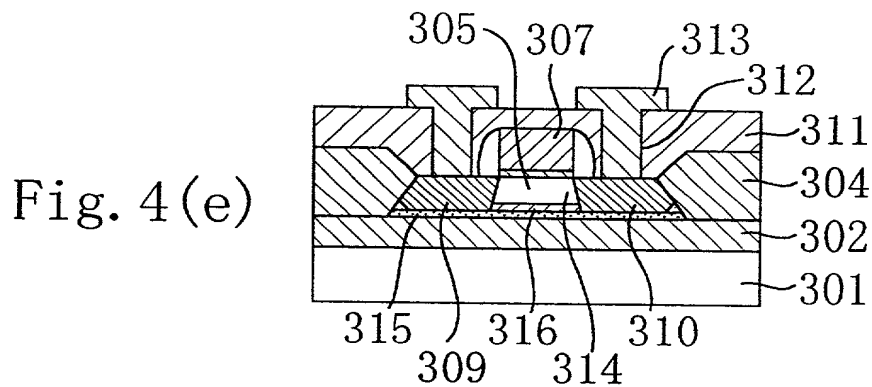
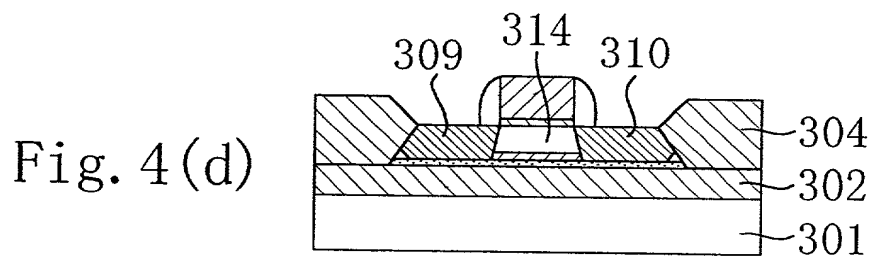
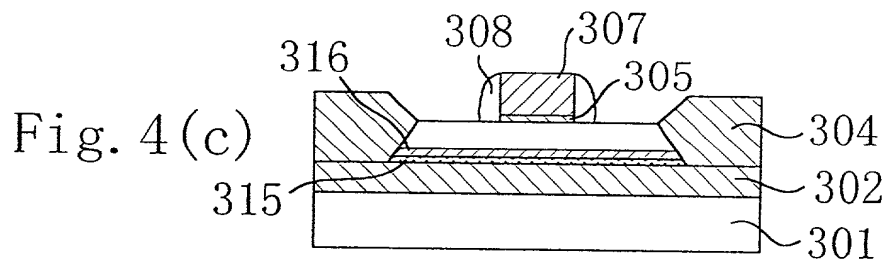
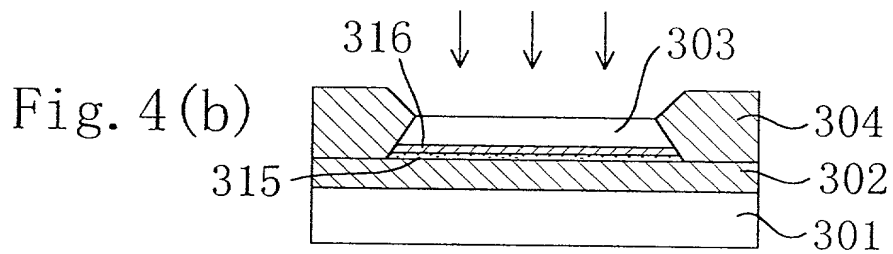
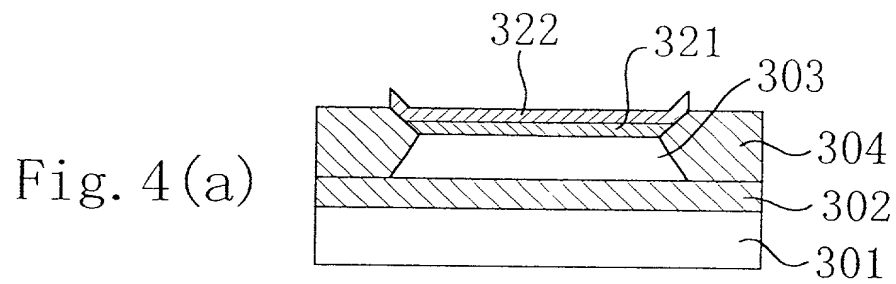
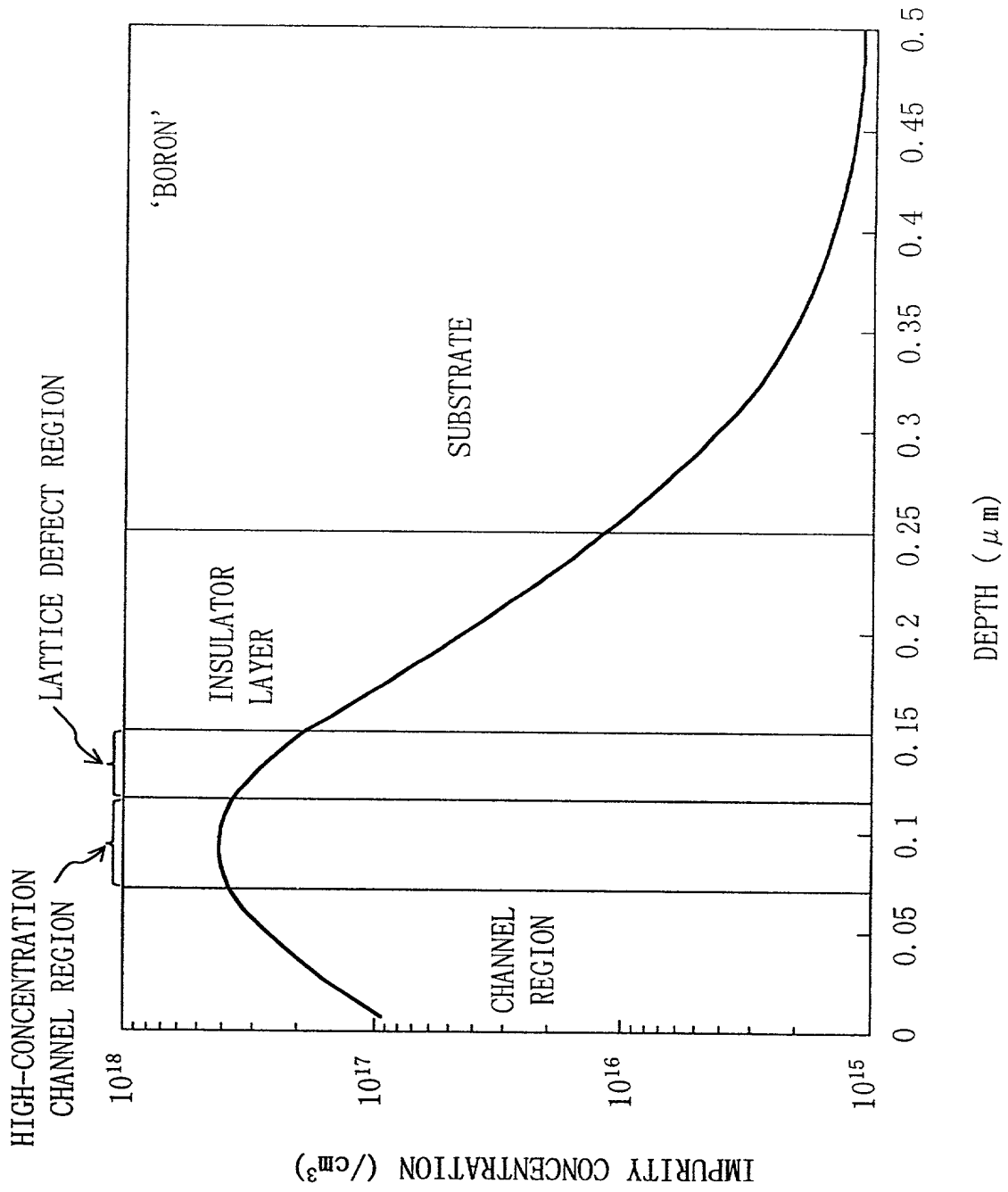
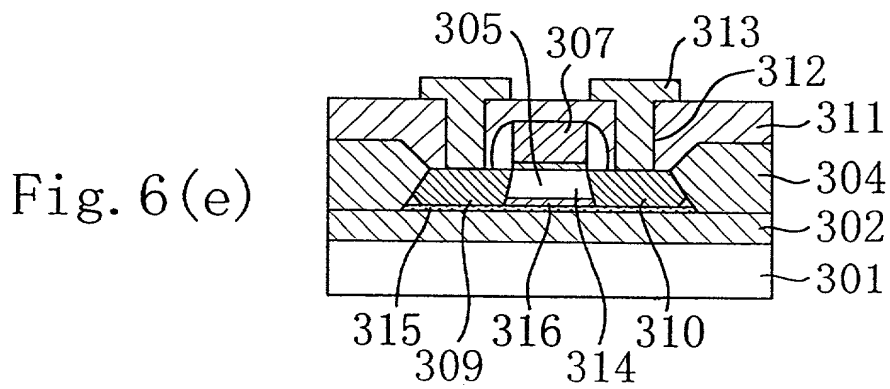
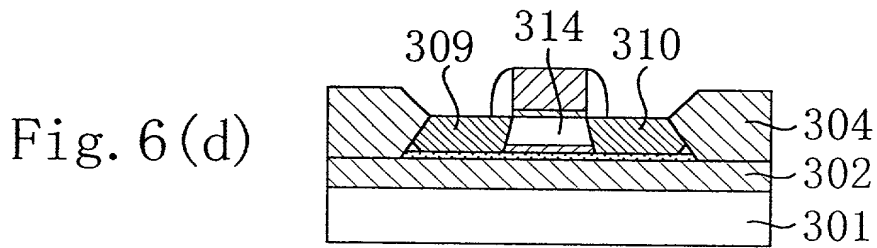
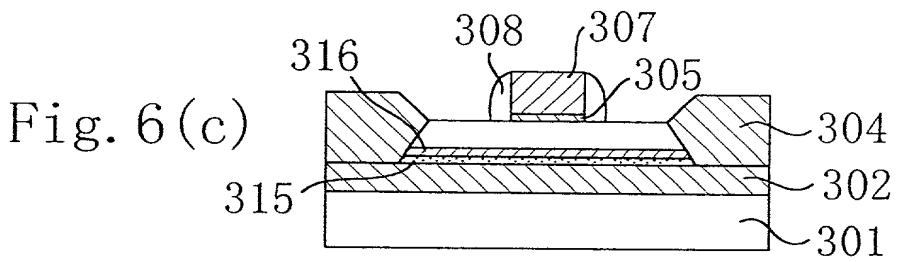
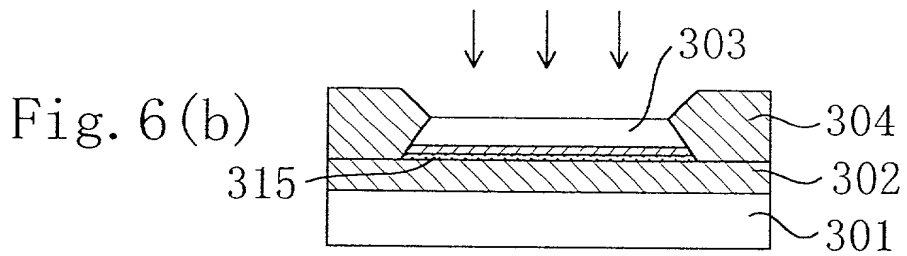
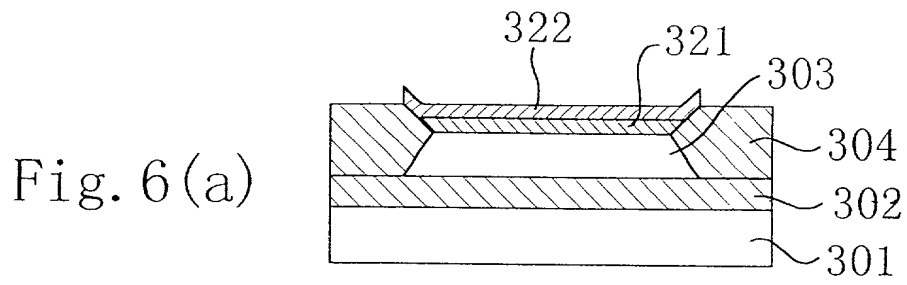
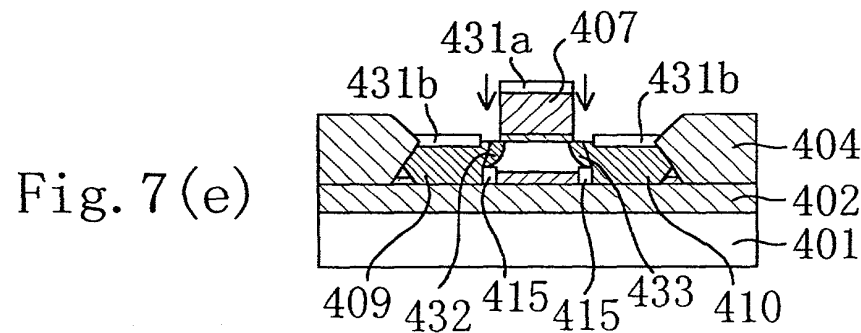
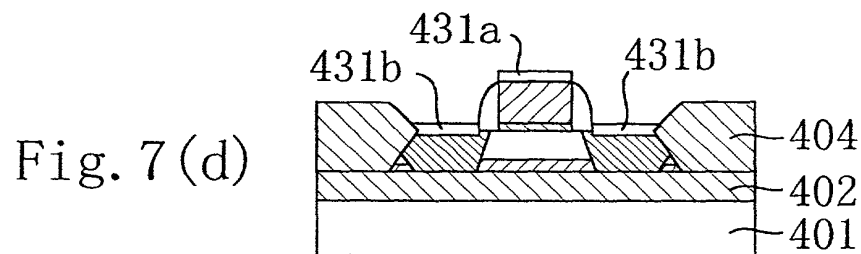
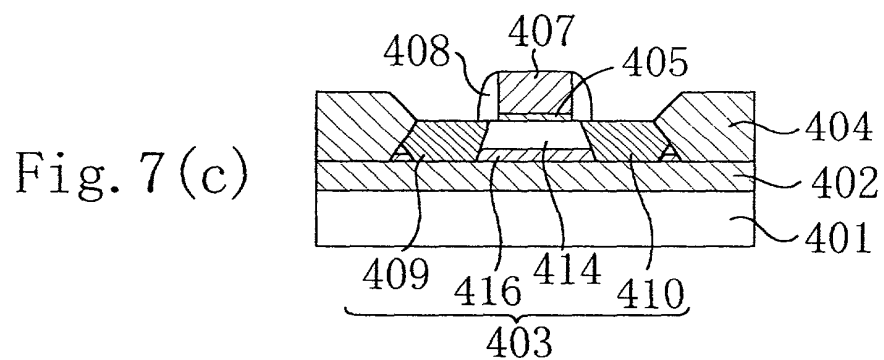
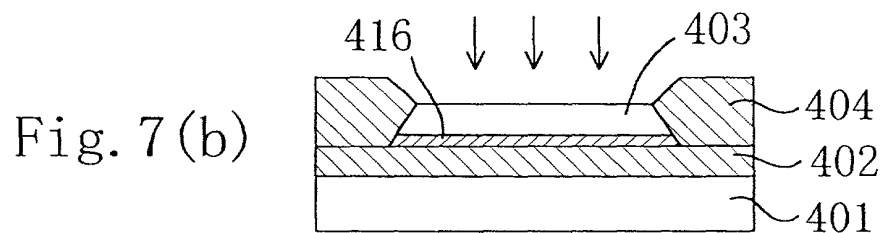
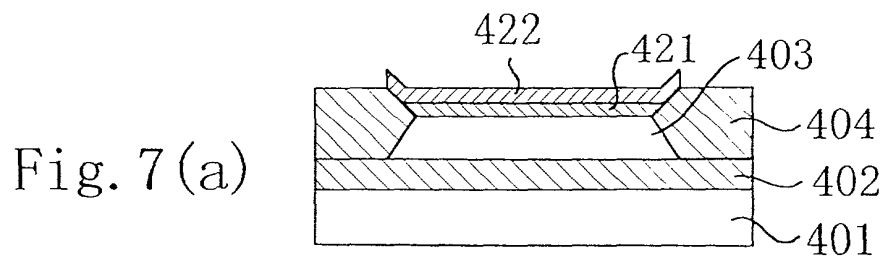


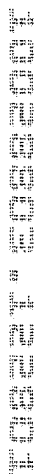
Fig. 5



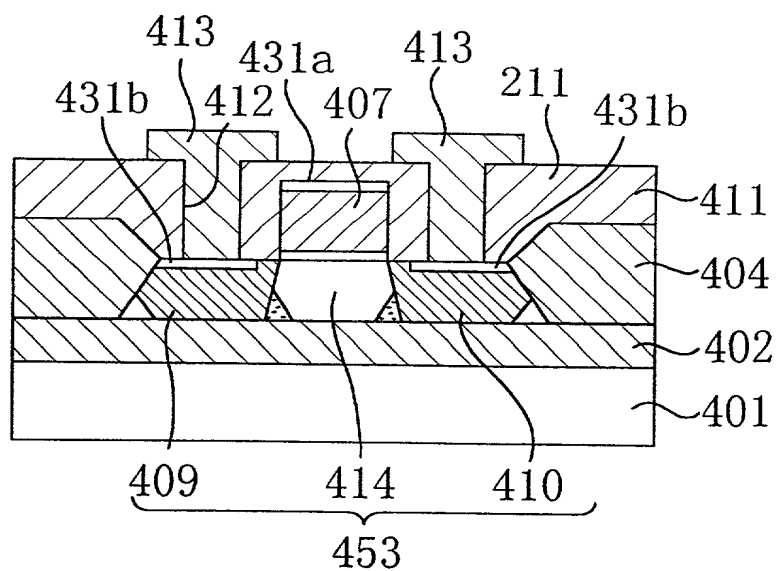






[illegible]

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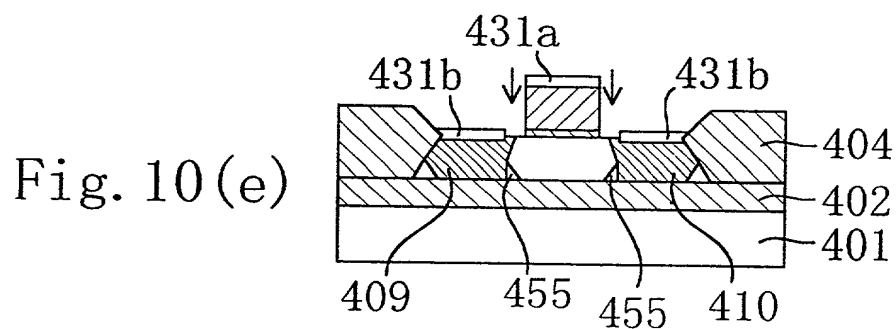
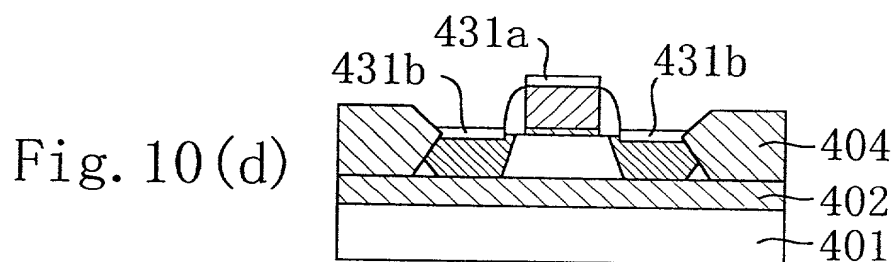
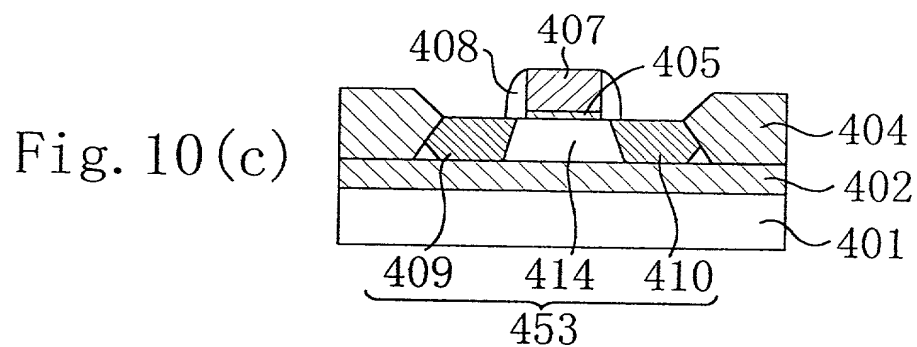
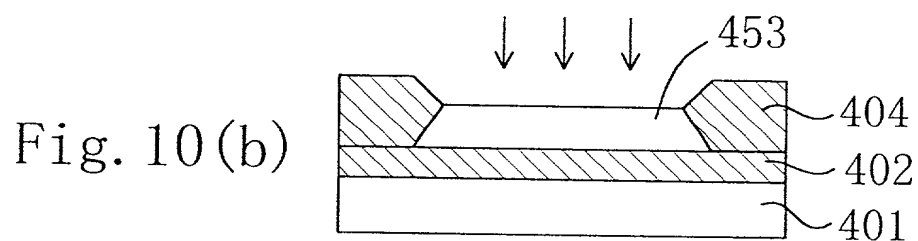
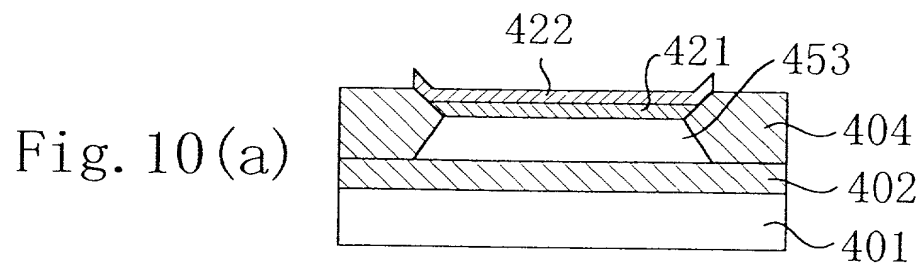


Fig. 11  
(PRIOR ART)

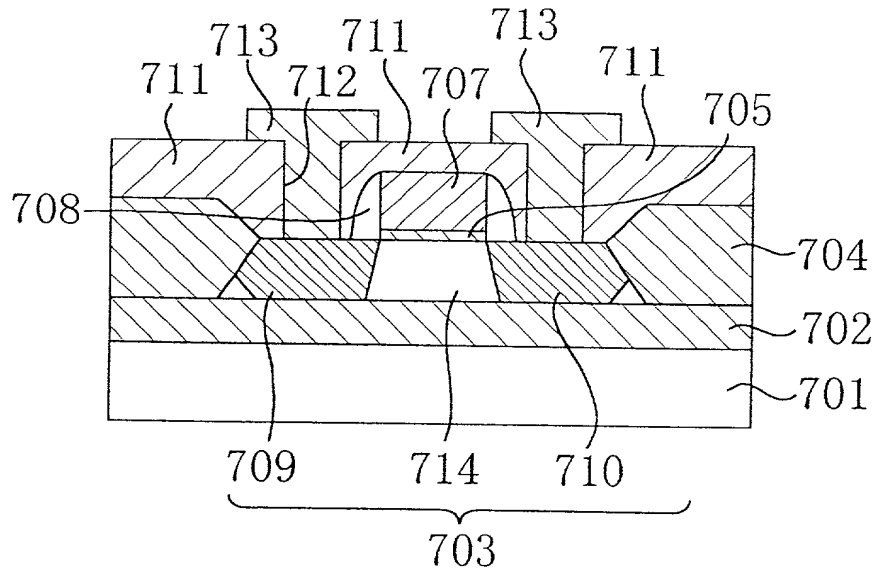


Fig. 12(a)  
(PRIOR ART)

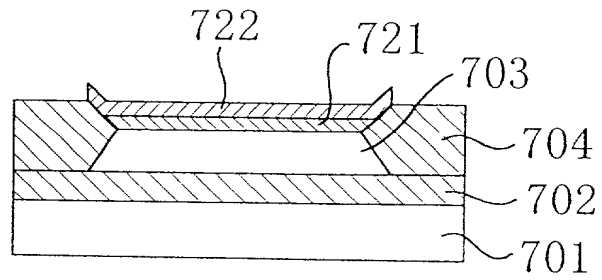


Fig. 12(b)  
(PRIOR ART)

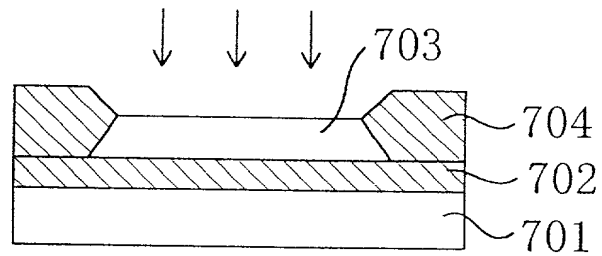


Fig. 12(c)  
(PRIOR ART)

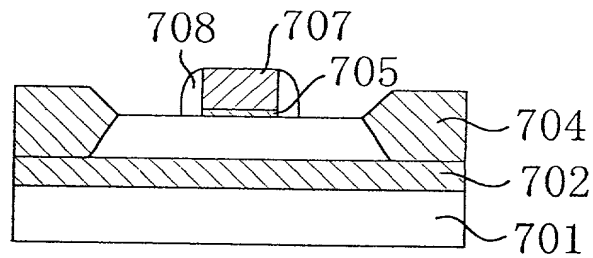


Fig. 12(d)  
(PRIOR ART)

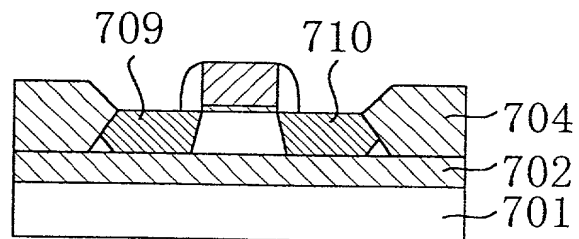


Fig. 12(e)  
(PRIOR ART)

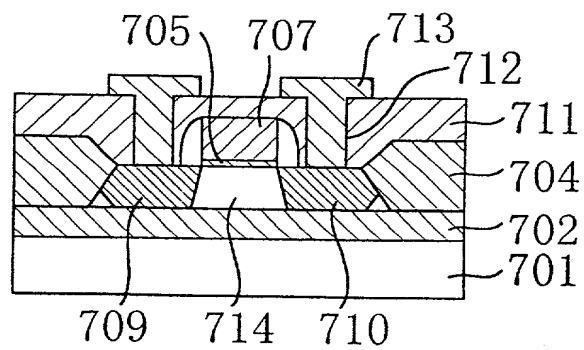


Fig. 13(a) (PRIOR ART)

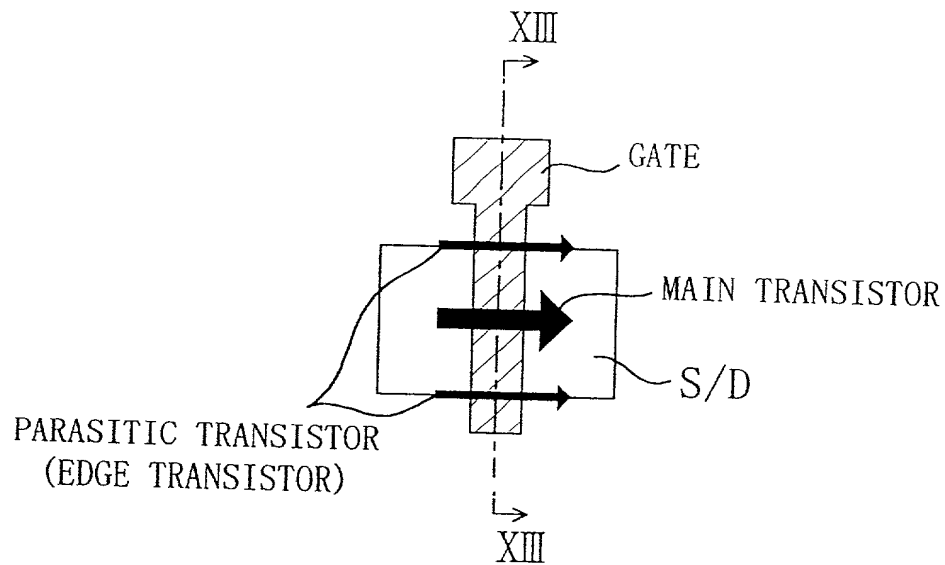


Fig. 13(b) (PRIOR ART)

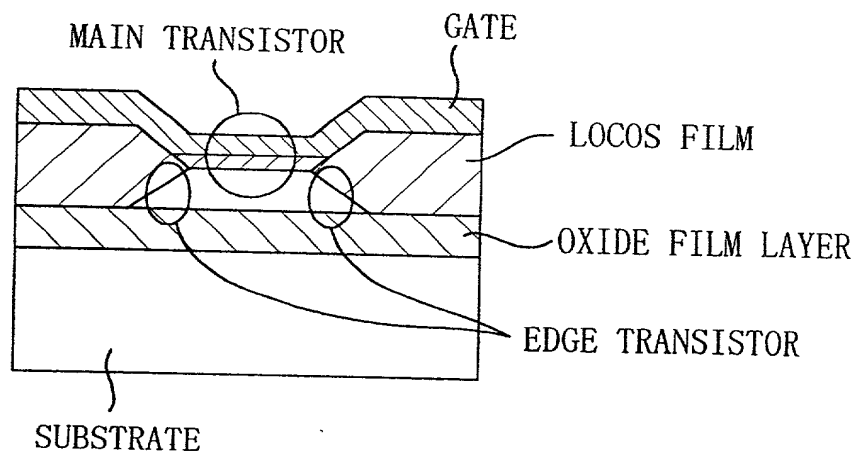
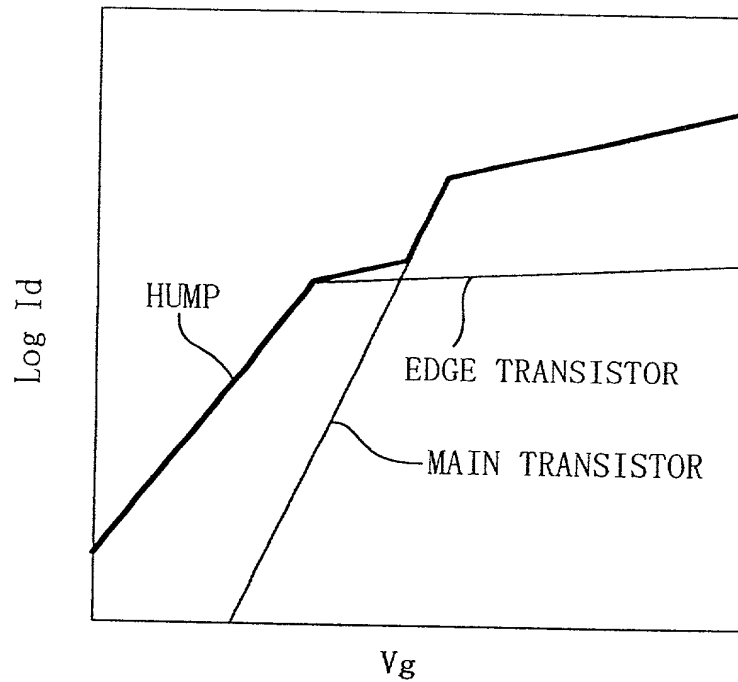


Fig. 14  
(PRIOR ART)



$I_d$ - $V_g$  PROPERTY

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H. NAKAOKA, et al. :  
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Filed: December 28, 2001 : Examiner: To be assigned  
For: SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

**REQUEST FOR APPROVAL OF DRAWING AMENDMENT**


Commissioner for Patents  
Washington, DC 20231

Sir:

Approval of the changes made to Figure(s) 11 - 14, as shown in red ink on the attached photocopies, is courteously solicited.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

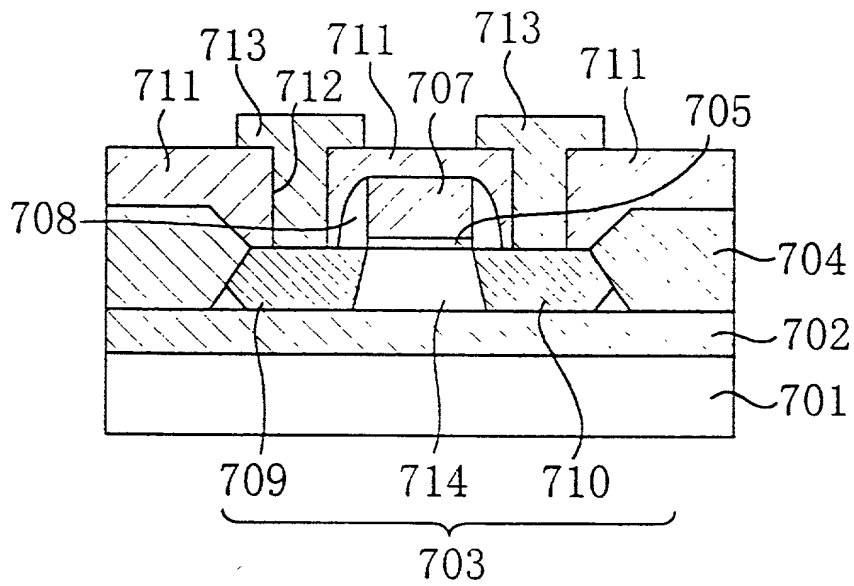
  
Michael E. Fogarty  
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**Date: December 28, 2001**  
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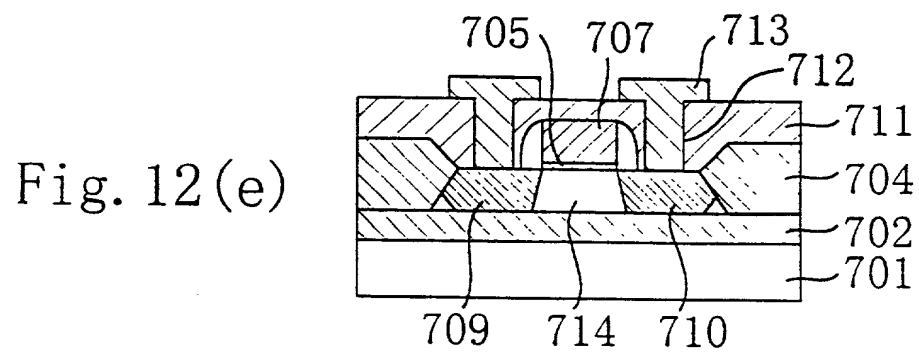
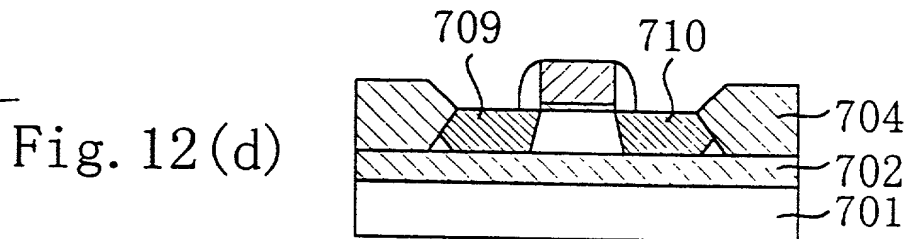
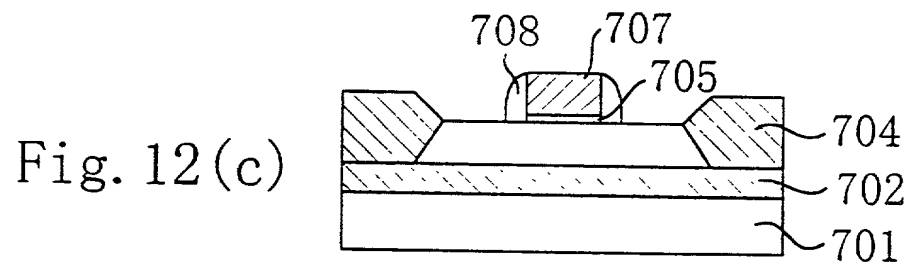
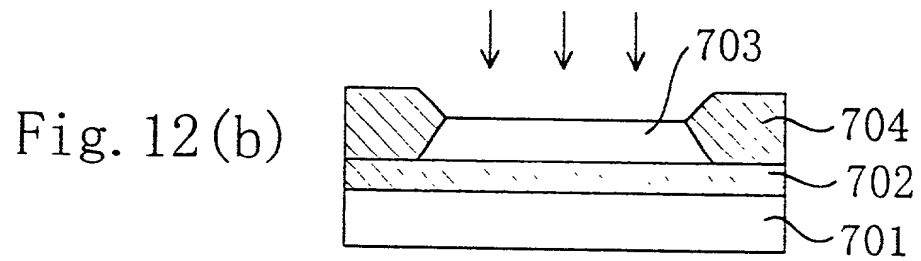
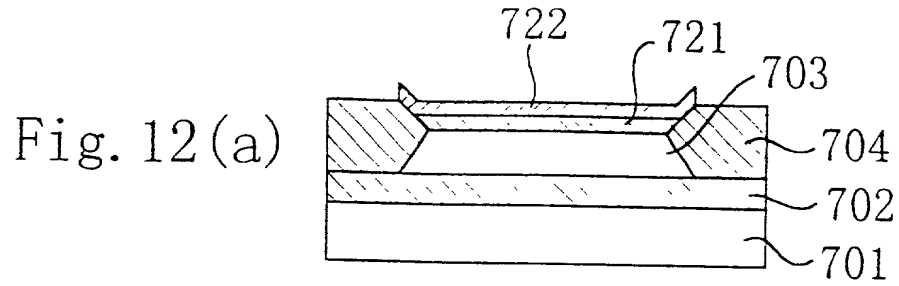


# PRIOR ART

Fig. 11



# PRIOR ART



PRIOR ART

Fig. 13(a)

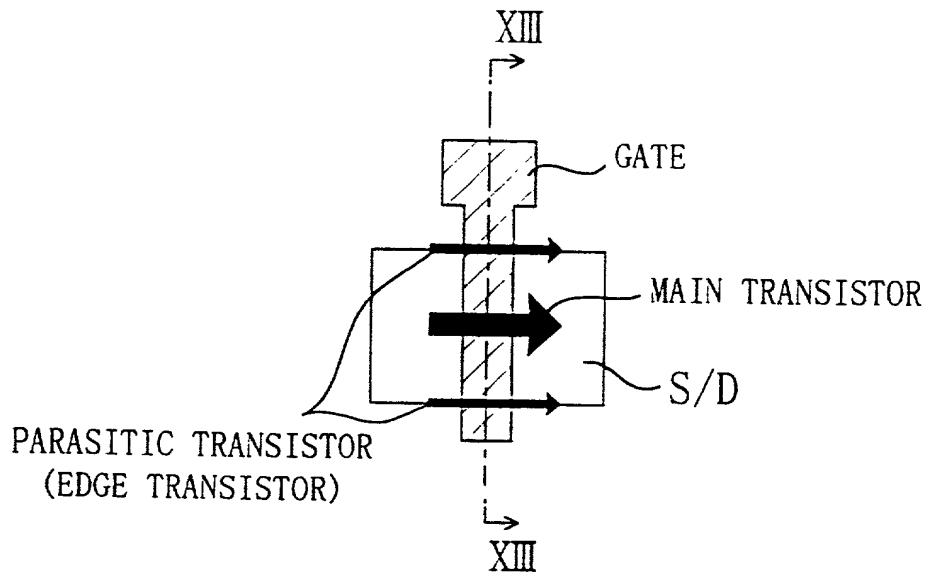
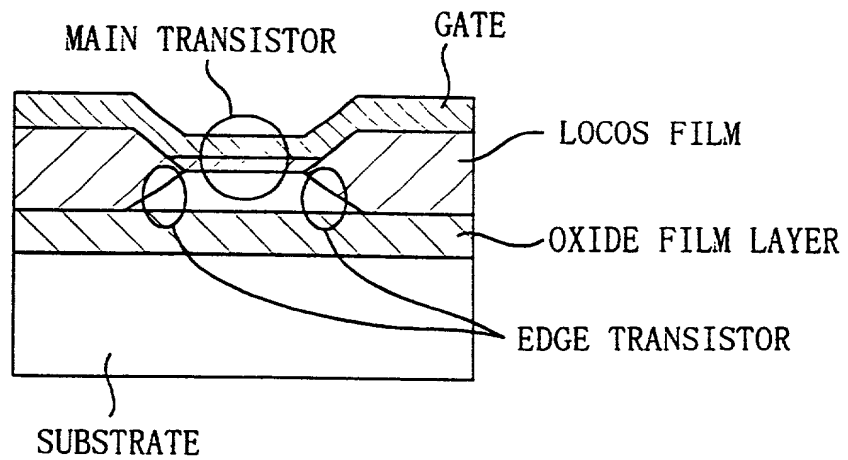


Fig. 13(b)



PRIOR ART.

Fig. 14

